Automated Microcode Synthesis

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A machine-independent approach to microprogram synthesis. On many modern computers, the processor control unit is microprogrammed rather than built directly in hardware. One of the tasks of the microcode is to decode the microinstruction into a control signal for the hardware. The microcode is usually stored in a ROM or PROM and accessed by the processor at runtime. Automated microcode synthesis presents the automatic synthesis of microprogrammed control units in the DIADES design automation system. The optimization of the microcode is incorporated. Automated synthesis of microprogrammed controllers in digital. Automated Microcode Synthesis details on Reading Cloud. Automated Synthesis of Microprogrammed Instruction Decoders by Genetic. Automated Microcode Synthesis 1998/11/26. Robert A. Mueller's book is extremely informative, and I was lucky enough to get the book during a public event.